

AMENDMENT TO CLAIMS

1-9. (Cancelled)

10. (Currently amended) A semiconductor device, comprising: ~~The semiconductor device according to claim 1, wherein:~~

a first circuit having a prescribed circuit function;

wherein a second circuit is formed so as to be able to be connected externally to the first circuit so as to give the first circuit a non-always-used particular function and to thereby allow the first circuit to perform the particular function;

the first circuit comprises a redundant circuit that is provided with wiring for forming prescribed logic blocks in the first circuit, and is configured in such a manner that its circuit function is determined by external redundancy setting; and

the particular function includes a circuit for setting the redundant circuit.

11. (Original) The semiconductor device according to claim 10, wherein:

the first circuit comprises a programmable gate array FPGA (field programmable gate array).

12-17. (Cancelled)

18. (Currently amended) A system device, comprising: ~~The system device according to claim 15, wherein:~~

a first device having a first circuit that provides a prescribed circuit function; and

a second circuit electrically connected to the first circuit of the first device, the system device;

wherein the second circuit gives the first circuit a non-always-used particular function and to thereby allow the first circuit to perform the particular function;

each of the first device and the second device comprises an exchange circuit for serially supplying or receiving control signals for giving the particular function and a register for storing control signals for giving the particular function; and

signals are serially exchanged between the first device and the second device, whereby the particular function is given to the first device.

19-23. (Cancelled)